



APPARATUS OF SEALING WAFER BACKSIDE FOR FULL-FACE
PROCESSING

Ashjaee et al.

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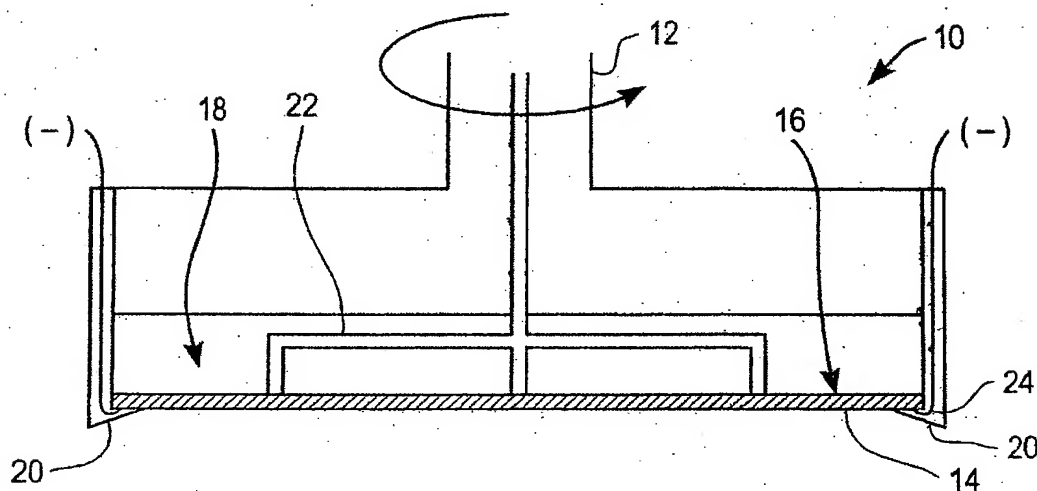


FIG. 1A (PRIOR ART)

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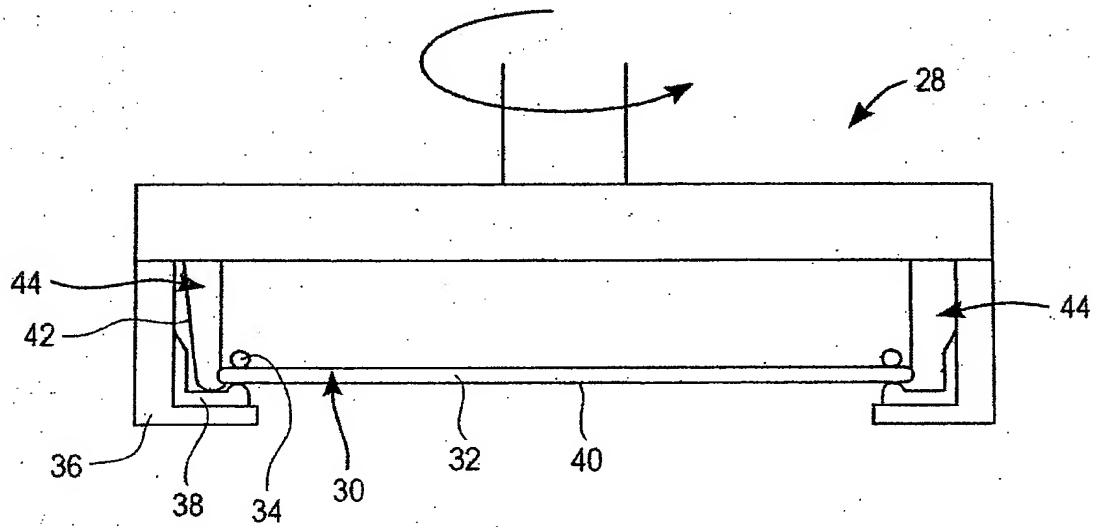


FIG. 1B (PRIOR ART)

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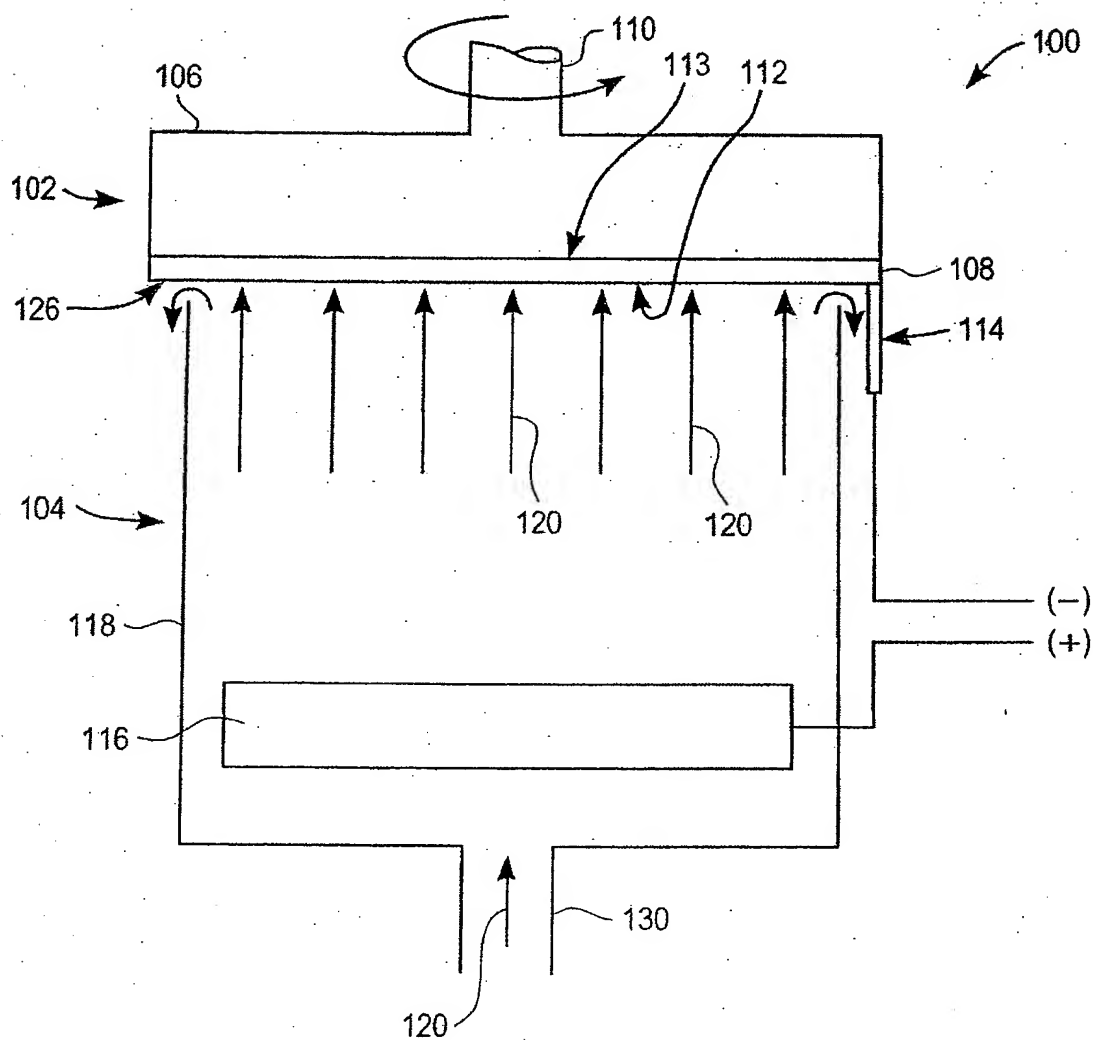


FIG. 2

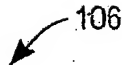


FIG. 3A

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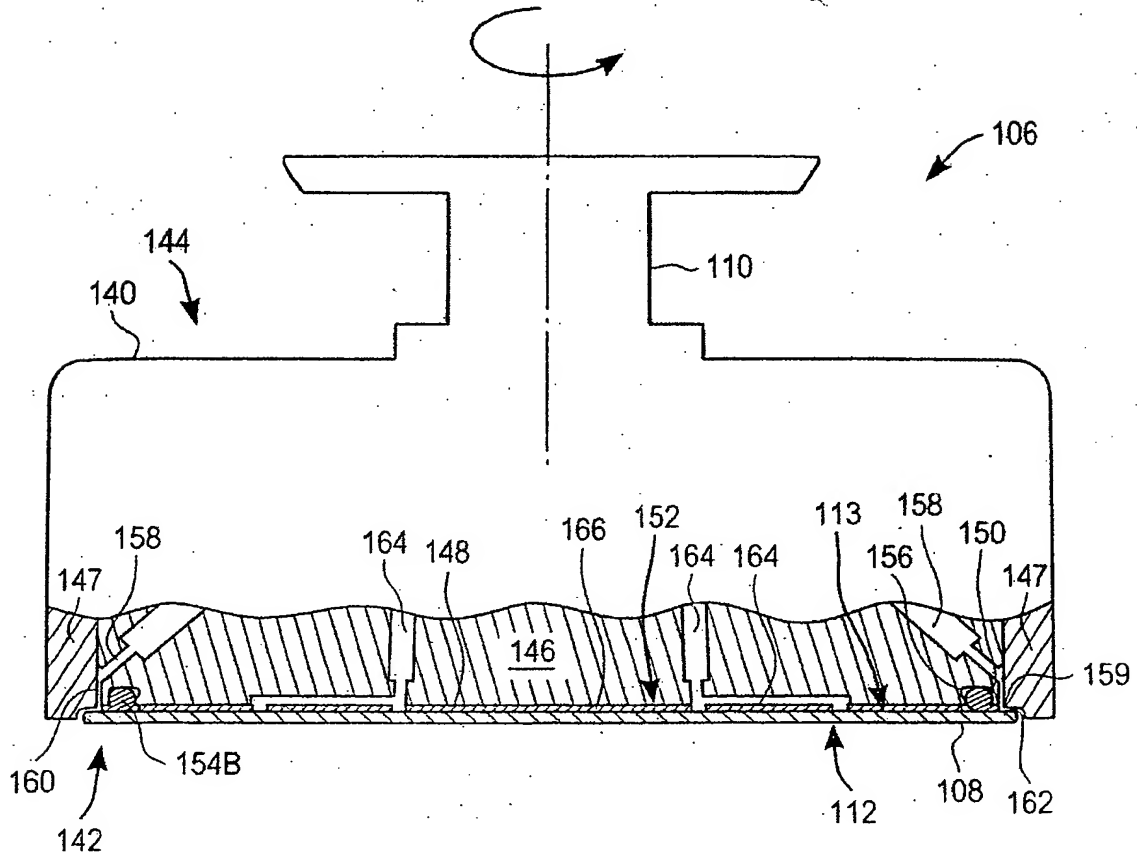


FIG. 3B

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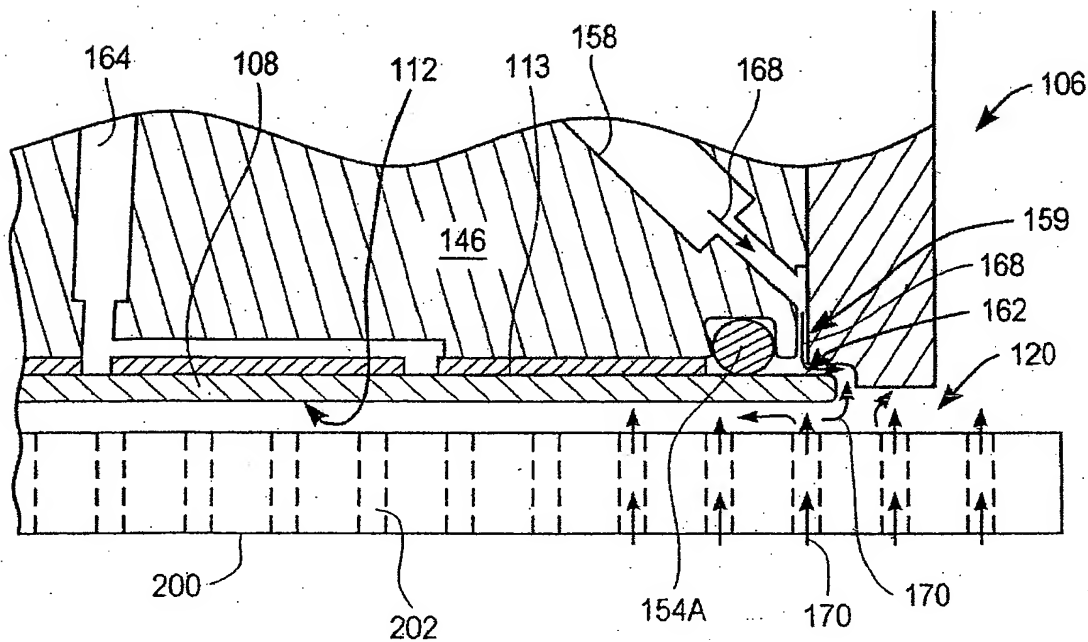


FIG. 4A

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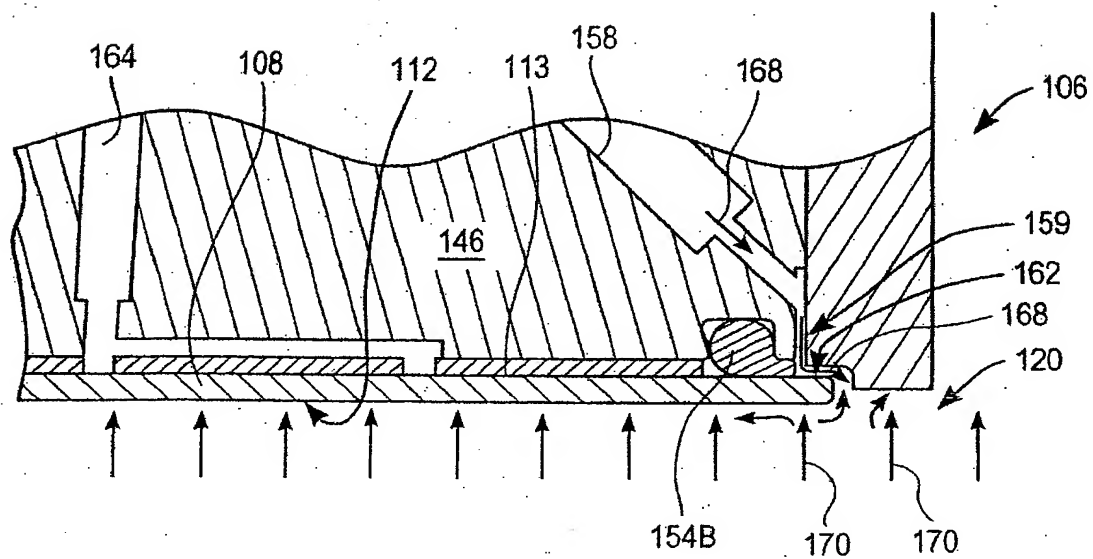


FIG. 4B

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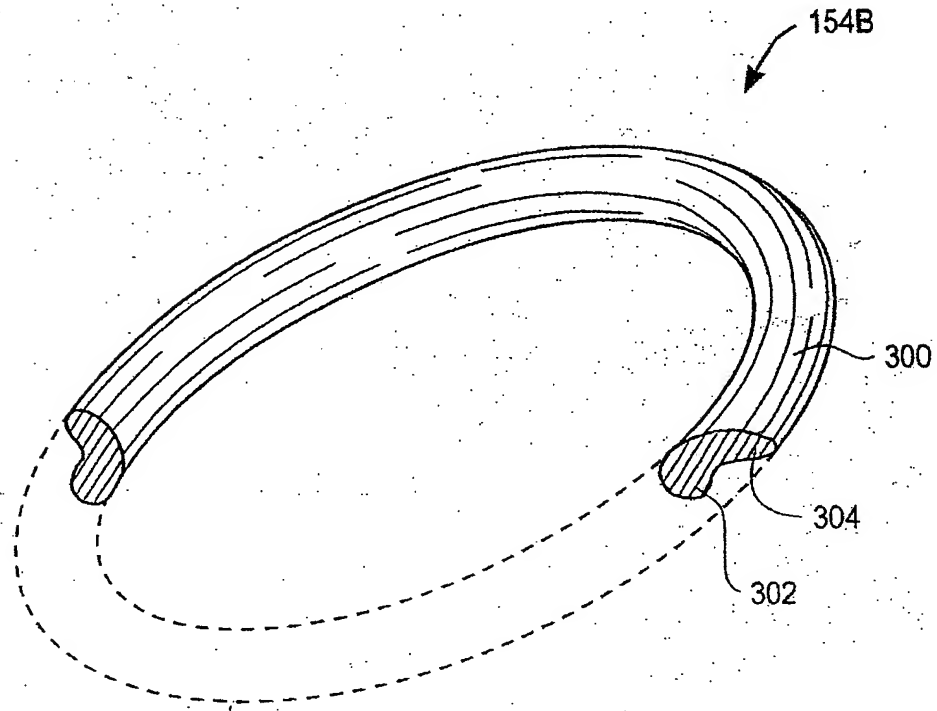


FIG. 5

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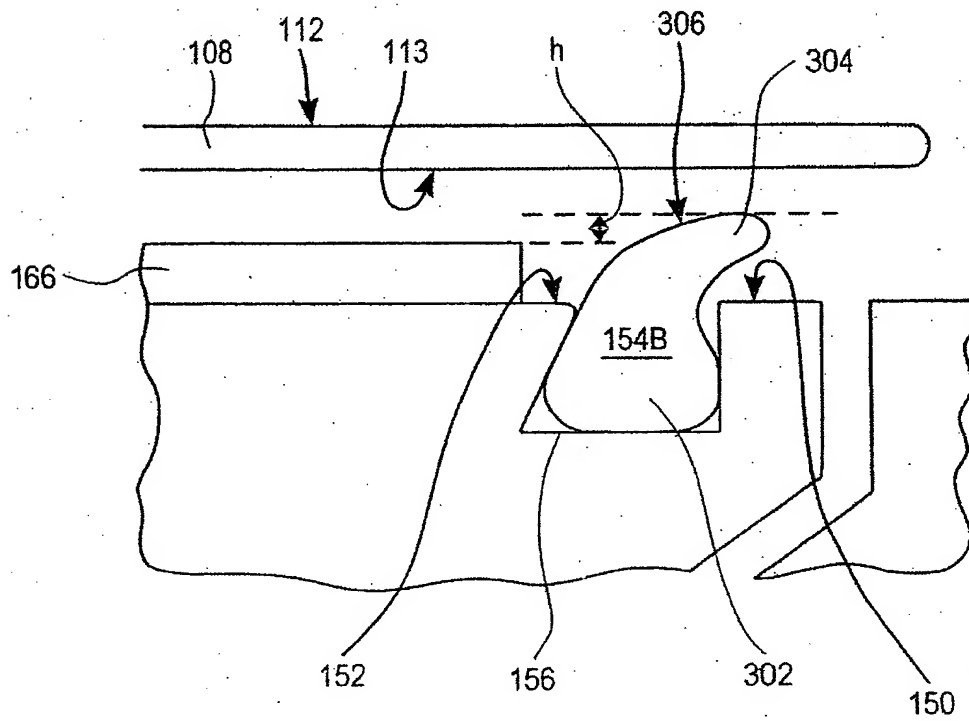


FIG. 6A

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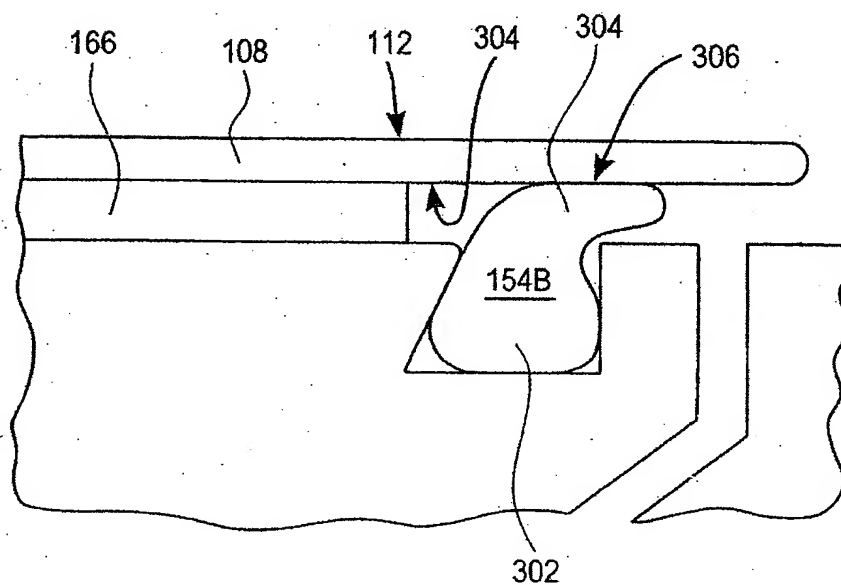


FIG. 6B

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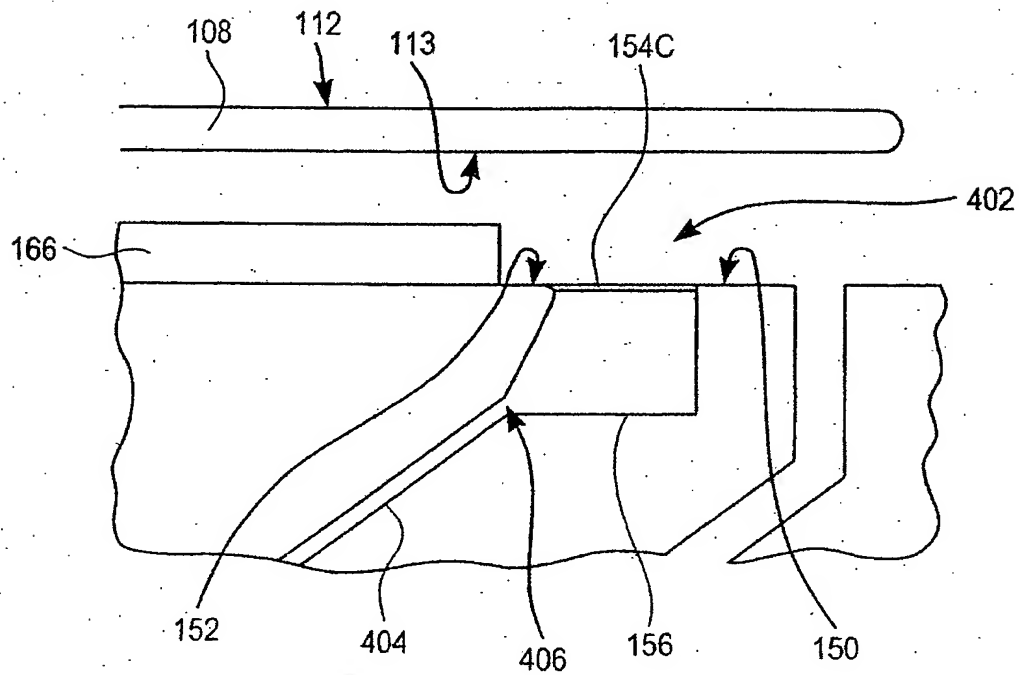


FIG. 7A

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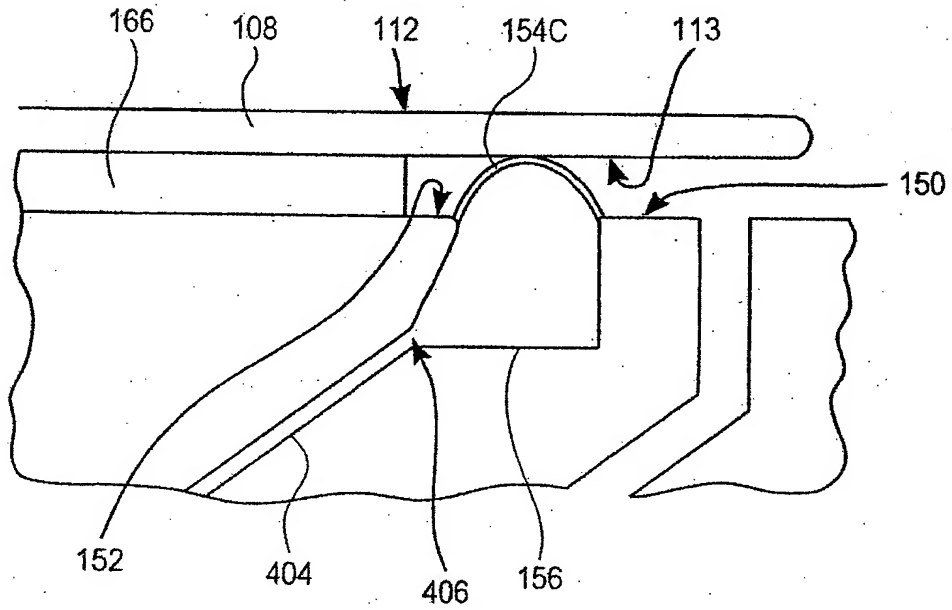


FIG. 7B

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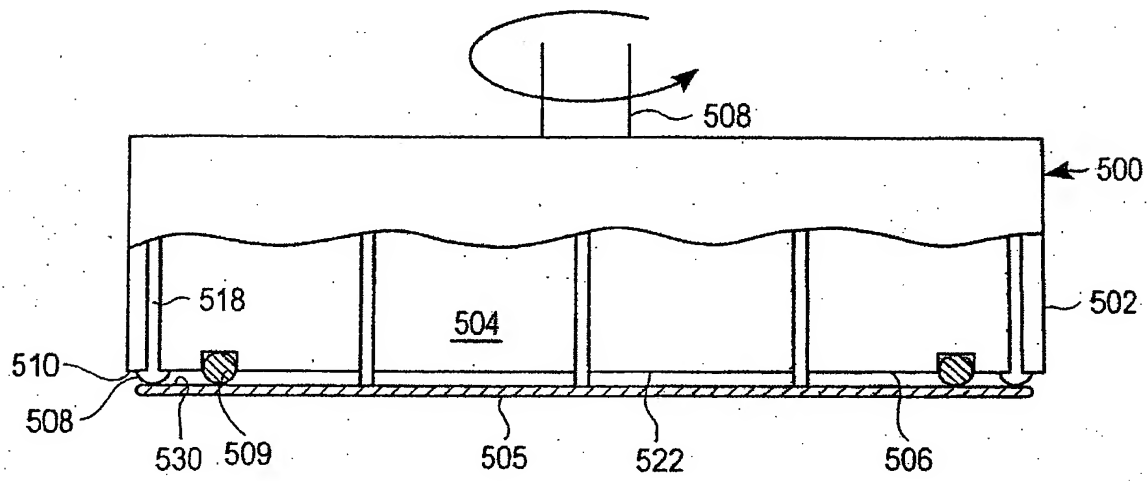


FIG. 8A

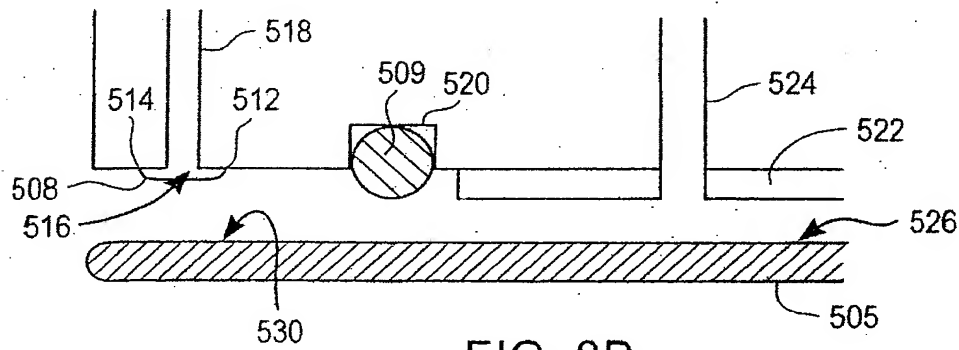


FIG. 8B

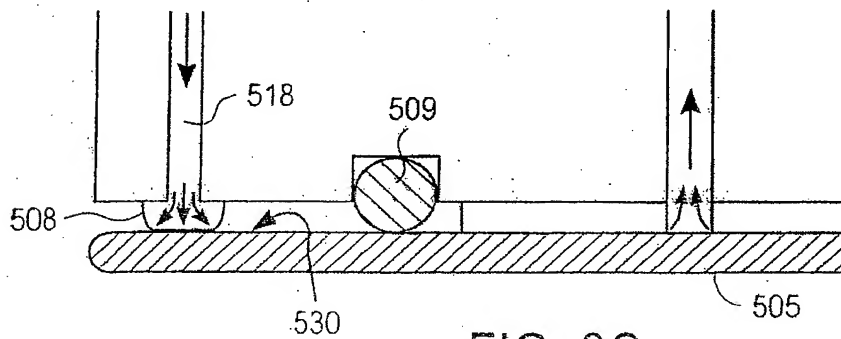


FIG. 8C

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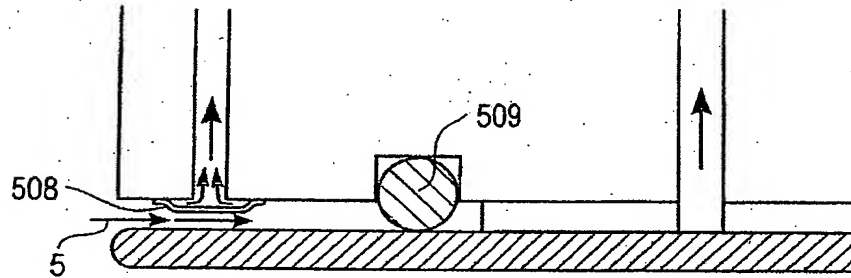


FIG. 8D

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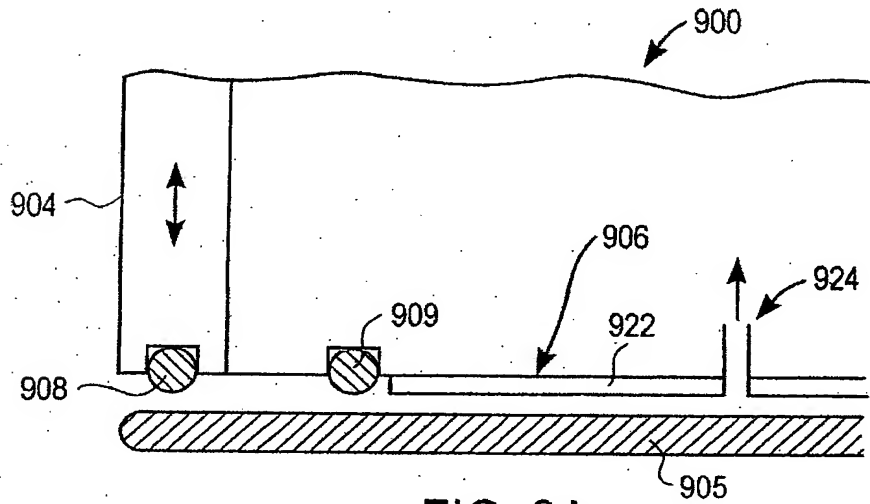


FIG. 9A

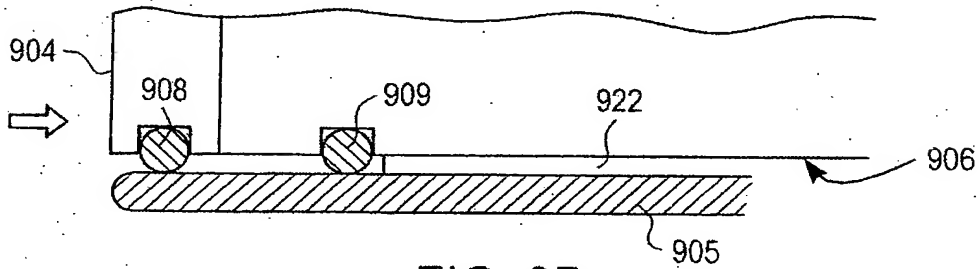


FIG. 9B

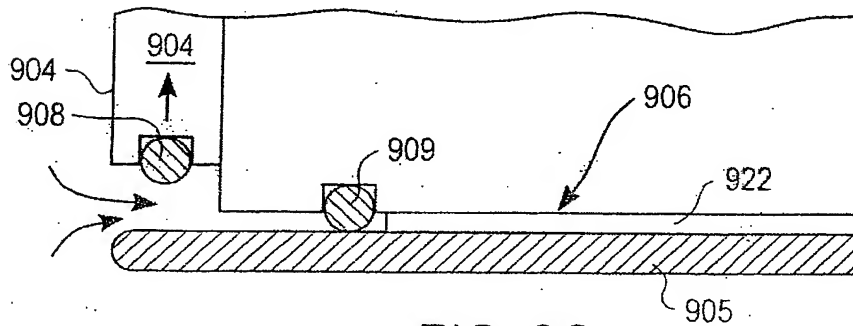


FIG. 9C